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TABLE OF CONTENTS

VOLUME 1

PLENARY SESSION - Paraconsistent Annotated Logic Program and its Application to Intelligent Control	23
Kazumi Nakamatsu	

Radio Communications, Microwaves, Antennas

1. Investigation of Second and Third Order Distortions Influence in the CATV/HFC	33
Oleg Panagiev	
2. Experimental study on availability of FSO system under a heavy snowfall	37
Nikolay Kolev and Tsvetan Mitsev	
3. Design of Cross Coupled Meander Folded Hairpin Resonator Filters	41
Marin Nedelchev	
4. For certain problems with DVB-T reception	45
Oleg Panagiev	
5. Comparative Performance Studies of Laboratory WPA IEEE 802.11b,g Point-to-Multipoint Links	49
José A. R. Pacheco de Carvalho, Cláudia F. F. P. Ribeiro Pacheco, Hugo Veiga, António D. Reis	
6. Two-way Doherty amplifier – asymmetry analysis and linearization	53
Aleksandar Atanasković, Kurt Blau, Nataša Maleš-Ilić, Aleksandra Đorić	
7. Environmental wireless sensor node	57
Vladimir Smiljaković, Siniša Randić, Uroš Pešović	
8. Effectiveness of Reed-Solomon and Convolutional Codes used in Digital Video Broadcasting....	61
Lidia Jordanova, Lyubomir Laskov and Dobri Dobrev	
9. Linearization of microwave power amplifier for broadband applications	65
Aleksandra Đorić, Aleksandar Atanasković, Nataša Maleš-Ilić, Bratislav Milovanović	
10. Modelling of a Coaxially Loaded Probe-Coupled Cylindrical Cavity using the Cylindrical TLM Method	69
Tijana Dimitrijević, Jugoslav Joković, Bratislav Milovanović	
11. Analysis of Electromagnetic Emissions from Printed Circuit Board in Enclosure Using TLM Method	73
Jugoslav Joković, Nebojša Dončov and Tijana Dimitrijević	

Telecommunication Systems and Technology

1. The level crossing rate of the ratio of product of two k-μ random variables and k-μ random variable	79
Časlav Stefanović, Danijel Đošić, Dušan Stefanović, Miloš Perić, Mihajlo Stefanović, Srđan Maričić	
2. Second order statistics of MRC receiver over α-μ multipath fading channels	83
Danijel Đošić, Časlav Stefanović, Stefan Panić, Nataša Kontrec, Petar Spalević, Negovan Stamenković	
3. Development of Mobile Backhaul and Transport Demands	87
Maja Kukulovska and Liljana Gavrilovska	

4.Active Time-slot Extension in Wireless Sensor Networks.....	91
Mirko Kosanovic, Mile Stojcev	
5.Modeling and Analyzing LTE Networks with EstiNet Network Simulator and Emulator.....	95
Stojan Kitanov and Toni Janevski	
6.BER Performance of IM/DD FSO System with PIN Photodiode Receiver over Gamma-Gamma Atmospheric Turbulence Channel.....	99
Milica Petković, Nemanja Zdravković, Bata Vasić and Goran Đorđević	
7.Preemptive and Non-preemptive Service of IoT Traffic Flows	103
Dimitar Atamian and Boris Tsankov	
8.Carrier Frequency Offset Problem Solving in the OFDM/MDPSK System	107
Slavimir Stošović, Nenad Milošević, Bojan Dimitrijević and Zorica Nikolić	
9.Overview of current trends in IPTV related FP7 projects.....	111
Biljana Veselinovska, Marjan Gusev, Toni Janevski	

Signal Processing

1.Dynamic compensation of the gyroscope bias offset.....	117
Rosen Miletiev, Radostin Kenov, Ivaylo Simeonov, Emil Iontchev	
2.Simulation of Codec for Adaptive Delta Modulation	121
Rumen Mironov	
3.Omnidirectional Sound Sources for Usage in a Small Anechoic Chamber	125
Dejan Ćirić, Marko Janković and Aleksandar Pantić	
4.Practical realization and analysis of shotgun microphone prototype	129
Marko Janković, Dejan Ćirić and Marko Stamenković	
5.EEG Sleep Spindles Identification Using Empirical Mode Decomposition and Morphological Operations	133
Yuliy Velchev, Deyan Milev and Kalin Dimitrov	
6.A Wavelet Based Approach for K-complexes Identification for Automated EEG Sleep Staging	137
Deyan Milev, Yuliy Velchev and Kalin Dimitrov	
7.Inharmonicity of Two-Tones In Contra Octave of Upright Piano	141
Zoran Milivojević, Milena Rajković and Dragan Milosavljević	
8.Tempo Map Retrieval from the MIDI Clock Stream	145
Lutshayzar Gueorguieff and Peter Antonov	
9.Customization of software for sound insulation prediction in buildings to national legislations – Case study: Slovenia	149
Draško Mašović, Nikola Arsić, Dragana Šumarac Pavlović and Miomir Mijić	
10.The influence of less available physical parameters on the sound insulation calculation according to EN 12354.....	153
Draško Mašović, Dragana Šumarac Pavlović and Miomir Mijić	

Digital Image Processing

1.Content-Based Facial Image Retrieval Using SIFT Descriptor with Reduced Number of Matched Keypoints.....	159
Nikolay Neshov	
2.Text Extraction from Complex Background Images.....	161
Nikolay Neshov, Ivo Draganov, Darko Brodic	
3.Estimation of the Global and Local Text Skew in the Old Printed Documents	165
Darko Brodić, Ivo Draganov, Dragan Milivojević, Viša Tasić	

4.New and advantageous approach for lossless compression of computer tomography image sequences	169
Peter Ivanov, Agata Manolova, Roumen Kountchev	

Computer Systems and Internet Technologies

1.Comparison of Open Source Cloud Platforms.....	175
Aleksandar Donevski, Sasko Ristov and Marjan Gusev	
2.Interactive Environment for Solving Multiple Objective Programming Problems GENS-IM	179
Leonid Kirilov, Krasimira Genova, Vassil Guliashki and Peter Zhivkov	
3.Managing Risk In Transmission System With Implemented Service Oriented Arhitecture	183
Nevenka Kiteva Rogleva, Vladimir Trajkovik, Vangel Fustik, Atanas Iliev and Dimitar Dimitrov	
4.Performance Analysis of Different Queuing Scheduling Disciplines for Internet Applications ...	187
Sarhan M. Musa, Mahamadou Tembely, Matthew N. O. Sadiku, and J. D. Oliver	
5.A study of open source PKI systems applicable into INDECT project.....	191
Nikolai Stoianov and Emil Altimirski	
6.Implementation of the Objects Queue of 20 elements for the File Cabinet Memory Method	195
Vladimir Stankovic and Kristina Stanisavljevic	
7.Implementation of Parallel LFSR for BIST	199
M. K. Stojčev, I. Ž. Milovanović, E. I. Milovanović, T. R. Nikolić	
8.Improving performance of geospatial data processing using OpenMP	203
Natalija Stojanović, Dragan Stojanović	
9.Acquiring Performability Metrics of e-Commerce Systems	207
Pece Mitrevski and Ilija Hristoski	
10.Web Services Performance on Commercial Virtual Environment (VMware ESX)	211
Goran Velkoski, Sasko Ristov and Marjan Gusev	
11.Using Petri Nets to Capture Search Behavior Patterns in the Context of Query Reformulation	215
Vesna Gega and Pece Mitrevski	
12.Dataflow Computing: Trend in HPC	219
Nenad Anchev, Blagoj Atanasovski, Sasko Ristov and Marjan Gusev	
13.QoS Routing Models in Mobile Applications that Implement Ad-Hoc Networking.....	223
Trajche Kocev, Pece Mitrevski and Tome Dimovski	
14.Cloud Solutions for Bug Reporting	227
Pano Gushev, Ana Guseva, Sasko Ristov and Marjan Gusev	
15.Optimization of DC/AC inverter driving.....	231
Goran Nikolić, Tatjana Nikolić, Branislav Petrović and Mile Stojčev	
16.A 900 MHz Self-Tunable Narrowband Low-Noise Amplifier.....	235
Goran Jovanovic, Darko Mitić, Mile Stojcev and Tatjana Nikolic	

Informatics and Computer Science

1.Evaluation of smartphone capabilities for efficient physical activity recognition.....	241
Nikola Jajac, Bratislav Predic and Dragan Stojanovic	
2.A Method for Estimation Camera Georeference in GIS-based Video Surveillance	245
Aleksandar Milosavljević, Dejan Rančić and Aleksandar Dimitrijević	
3.Computation of Best Fixed Polarity Reed-Muller Transform on Multicore CPU Platform.....	249
Miloš Radmanović	
4.Hybrid Evolutionary Algorithm for Integer Multiple-Objective Optimization Problems.....	253
Vassil Guliashki, Krasimira Genova, Leoneed Kirilov	

5.Reasoning-enabled Semantic E-Learning Approach.....	257
Martin Jovanović and Dejan Todosijević	
6.Efficient Parallel Computation of the Galois Field Expressions for Ternary Logic Functions	261
Dušan Gajić and Radomir Stanković	
7.Calculation and Visualization of Electromagnetic Field Strength Estimate using Real Terrain Model	265
Vladan Mihajlović, Marko Kovačević, Aleksandar Milosavljević and Dejan Rančić	
8.Solving Kakuro puzzle – comparison of deterministic approaches.....	269
Stojanche Panov and Saso Koceski	
9.Cache Misses Challenge to Modern Processor Architectures.....	273
Milco Prisaganec and Pece Mitrevski	
10.Machine Learning Based Classification of Multitenant Configurations in the Cloud.....	277
Monika Simjanoska, Goran Velkoski, Sasko Ristov and Marjan Gusev	
11.Buffer Management in High-performance Routers.....	281
Dragi Kimovski and Atanas Hristov	

Electronics

1.VHDL-AMS Description of Digitally Programmable Gain Amplifiers through SPI	287
Marieta Kovacheva and Ivailo Pandiev	
2.Concurrent X-fault simulator – problems and decision	291
Pavlinka Radoyska and Kamen Fillyov	
3. Design of GPS-based Wild Animal’s Tracking System with Reduced Size and Weight	295
Eltimir Stoimenov, Tsvetan Shoshkov, Rosen Miletić, Ivailo Pandiev	
4.Finite Element Analysis for Multiconductor in Non-Homogenous Multilayered Dielectric Media	299
Sarhan M. Musa, Matthew N. O. Sadiku, and J. D. Oliver	
5.Computer-Aided Parameter Extraction of Behavioral RF Inductor Models.....	303
Elissaveta Gadjeva	
6.Effectiveness of the Verilog-A Noise Macromodel of Current Feedback Operational Amplifier	307
Georgi Valkov and Elissaveta Gadjeva	

Energy Systems and Efficiency

1.Optimal experiment for determination of the thermo physical properties on materials with low thermal conductivity.....	313
Zore Angelevski, Cvete Dimitrieska, Silvana Angelevska and Ivo Kuzmanov	
2.Daily Optimal Operation of Cascade Hydro Power Plants With Small Storage Capacities.....	317
Anton Causevski and Sofija Nikolova-Poceva	
3.Distributed Renewable Energy and Conviviality.....	321
Aleksandar Malecic	
4.Probabilistic Assessment of the Impact of Renewable Energy Sources on the Power Flows of Medium Voltage Grids.....	325
Nikolay Nikolaev	
5.Optimal Modules Deployment in Large-Scale Photovoltaic Plants.....	329
Dimitar Dimitrov, Atanas Iliev and Nevenka Kiteva Rogleva	
6.Benefits of 6 kV Smart Grid Implementation in Open Cast Coal Mine Suvodol - REK Bitola....	333
Ljupco Trpezanovski and Jove Gjorgijovski	
7.Analysis of the Grounding System of the Thermal Power Plant Oslomej	337
Nikolce Acevski, Elena Stojkoska	

8.The State of Renewable Electricity – Worldwide, in EU and in R.Macedonia.....	341
Gordana Janevska	

Control Systems

1.Analyzing the number and the nature of the injuries in a industrial system from Bitola, R. Macedonia	347
Ivo Kuzmanov, Silvana Angelevska and Zore Angelevski	
2.Trajectory Tracking Control for the Slew Motion of a Dragline Excavator	351
Rosen Mitrev and Plamen Petrov	
3.Global path planning algorithm for mobile robots.....	355
Stojanche Panov and Saso Koceski	
4.Efficient RF voltage transformer with bandpass filter characteristics	359
M.Moreira, J.Bjurström, I.Katardjiev and V.Yantchev	
5.Dynamic Models for Induction Motor Drives for Heavy Duty Regimes.....	361
Dragan Vidanovski and Slobodan Mirčevski	

Measurement Science and Technology

1.Investigation of Memory Effect by Measurement of Time Delay of Electrical Breakdown in Commercial Gas-filled Surge Arresters.....	367
Momčilo Pejović, Nikola Nešić, Milić Pejović and Nataša Bogdanović	
2.Smart sensor network for ergonomic evaluation of working environment	371
Teodora Trifonova, Valentina Markova, Ventseslav Draganov, Krasimira Angelova and Vasil Dimitrov	
3.Measurements and Test Performance for Integrated Digital Loop Carrier for White Noise Impairment with Fast Mode	375
Sarhan M. Musa, Mohammed A. Shayib, Matthew N. O. Sadiku and J. D. Oliver	
4.Testing Procedure applied to Virtual Instrument for Analysis of the Power Quality Disturbances.....	379
Milan Simić, Dragan Živanović, Dragan Denić and Goran Miljković	
5.Improved Pseudorandom Absolute Position Encoder.....	383
Goran Miljković, Dragan Denić, Milan Simić, Aleksandar Jocić, Jelena Lukić	

Engineering Education

1.Trends in Increasing the Channel Capacity of FSO Systems	389
Yordan Kovachev and Tsvetan Mitsev	
2.A New Curriculum Design for an Engineer-Constructor Study Program	393
Tale Geramitcioski, Cvetanka Mitrevska, Vangelce Mitrevski and Pece Mitrevski	

Poster 1 - Radio Communications, Microwaves, Antennas

1.Topologies of Wireless Sensor Networks	399
Zlatan Ganev	
2.QWS Surge Protectors Testing Using Random High-Voltage Pulses Modeling	403
Kliment Angelov and Miroslav Gechev	
3.Impact of Laser Beam Divergence on Power Design of Free Space Optics Communication Systems	407
Boncho Bonev	

4.Methods of Coordinates Determination in Wireless Sensor Networks	409
Zlatan Ganev	
5.Statistical study of dispersion properties of the CATV reverse channel.....	413
Ilija Iliev and Marin Nedelchev	
6.System for monitoring and management of energy efficiency in public buildings	417
Emil Altimirski, Nicola Kaloyanov, Plamen Vichev, Veselin Plamenov	
7.Methods for Determination of Coordinates in Two-Dimensional Navigation System by Measuring the Delay of the Signal.....	421
Emil Altimirski, Petko Simeonov	
8.Efficient Neural Model for Estimation of the Microwave Antenna Noise Temperature.....	425
Ivan Milovanovic, Zoran Stankovic, Marija Agatonovic and Marija Milijic	
9.2D DOA Estimation of Two Coherent Sources based on RBF Neural Networks	429
Marija Agatonovic, Zoran Stankovic, Bratislav Milovanovic, Ivan Milovanovic and Nebojsa Doncov	
10.Statistical analysis of multiple reflections in single mode waveguides	433
András Fehér and Szilvia Nagy	
AUTHOR INDEX	437

VOLUME 2

Poster 2 - Telecommunications Systems and Technology

1.Investigate common work of IP software phone systems and PSTN equipment.....	455
Todorka Georgieva and Borislav Necov	
2.Integration of optical and wireless networks under the Radio-over-Fiber concept.....	459
Suzana Miladic	
3.Optical Line Terminal Process modeling.....	463
Stela Kostadinova and Rozalina Dimova	
4.Development of algorithm and simulation program for audio and video information quality estimation in multimedia systems.....	467
Kalina Peeva, Aleksander Bekiarski and Snejana Pleshkova	
5.Comparative Analysis of Modern Wireless Communication Systems Relevant to Smart Metering	471
Mariana Shotova, Georgi Nikolov and Vencislav Valchev	
6.Mobile Wireless Sensor Networks Localization	475
Vasil Dimitrov, Rozalina Dimova and Teodora Trifonova	
7.Laboratory SCADA – System for Control on Railway Traffic.....	479
Emiliya Dimitrova	
8.Channel Capacity of Dual SC Diversity System Based on Desired Signal Decision Algorithm in Microcell.....	483
Aleksandra Panajotović, Mihajlo Stefanović, Dragan Drača and Nikola Sekulović	
9.Simulation of Effects of Group Velocity Dispersion on Gaussian Pulse Propagation through Optical Fiber	487
Petar Spalević, Branimir Jakšić, Aleksandar Marković, Zoran Todorović and Vladislav Simić	
10.Algorithm for modular exponentiation in public key cryptosystems.....	491
Plamen Stoianov	
11.Energy Efficient Add/Drop Approach for Heterogeneous Networks.....	495
Oleg Asenov, Pavlina Koleva, Vladimir Poulkov	

Poster 3 - Signal Processing

1.A Variational Approach of Optimization the Signal Form in the Radio Communication Systems	501
Galina Cherneva, Elena Dimkina	
2.Synchronization in Radio Communication Systems with Pseudo Random Restructuring Operation.....	503
Antonio Andonov and Filip Iliev	
3.The Reduction of Rotating Element Noise Using Active Noise Control.....	505
Zoran Milivojević and Violeta Stojanović	
4.Investigation of second-order digital filter structures having low sensitivity to parasitic effects.	509
Maria Nenova	

Poster 4 - Computer Systems and Internet Technologies

1.A Methodology of Developing Interoperable Electronic Business in the Transport Sector.....	515
Slađana Janković, Snežana Mladenović, Marko Vasiljević, Irina Branović, Slavko Vesković	
2.Recommendation in E-Learning Based On Learning Style	519
Aleksandar Kotevski, Gjorgi Mikarovski and Ivo Kuzmanov	
3.Analysis and Classification of Robot Control Algorithms	523
Maya Todorova	
4.Elaboration of Internet of Things Security Functional Model	527
Evelina Pencheva	
5.Internet of Things in Healthcare Applications.....	531
Evelina Pencheva, Ivaylo Atanasov, Raycho Dobrev	
6.Determining the importance of the usability attributes of Web-based GIS applications.....	535
Nebojša Djordjević, Dejan Rančić	
7.Implementation of LMS in the Education in the Field of Programming	539
Niko Naka, Snezana Savoska and Josif Petrovski	
8.Adaptive vs. Non-adaptive e-Learning Systems – a Petri Net-based Evaluation Approach	543
Emilija Spasova Kamceva and Pece Mitrevski	
9.Content Management Systems – Unleashed Possibilities	547
Jove Jankulovski, Mimoza Anastoska-Jankulovska and Pece Mitrevski	
10.Appropriate Learning Tools and Approaches According to the Different Learning Styles and Collaboration Skills of the Students.....	551
Donika Valcheva and Margarita Todorova	
11.Optimal Design of Elements in Confirmation of Panel Buildings	555
Vassil Guliashki, Chavdar Korsemov, Hristo Toshev, Leoneed Kirilov and Krassimira Genova	
12.Modification of Algorithms to Control of Mobile Object.....	559
Maya Todorova	
13.Creating a virtual reality application from Memorial Museum “11th October” – Prilep	563
Boban Mircheski, Igor Nedelkovski, Aleksandra Lozanovska and Jove Pargovski	
14.Improved Data Transfer for Wireless Meteorological Stations.....	567
Orlin Stanchev, Emilian Bekov and Vencislav Valchev	
15.Interoperability of Cloud and Mobile Services	571
Aleksandar Bahtovski and Marjan Gusev	

Poster 5 - Digital Image Processing

1.Adaptive Vision System	577
Rosen Spirov and Neli Grancharova	
2.An Approach for Position Detection of Industrial Objects	581
Veska Georgieva and Plamen Petrov	
3.Approaches for Texture Image Creation	585
Daniela Ilieva	
4.Coding of a Video with the Inserted Watermark using H.264/AVC Coder	589
Zoran Veličković and Zoran Milivojević	
5.3D Modelling from video	593
Svetlana Mijakovska, Igor Nedelkovski	
6.Automated Vegetation Classification for LANDSAT 7 Multispectral Images	597
Dragan Stevic, Igor Hut, Nikola Dojčinović and Jugoslav Joković	

Poster 6 - Informatics and Computer Science

1.Numerical Experiments for the Study of the Influence of Wavelength in Laser Impact onto Metals and Alloys	603
Nikolay Angelov	
2.Similarity search in text data for Serbian language	607
Ulfeta Marovac, Adela Crnisanin, Aldina Pljaskovic, Ejub Kajan	
3.Optimization of Vehicle Maintenance Concept Using Simulation	611
Ivan Djokic, Ljubomir Lazic, Aldina Pljaskovic, Aleksandra Pavlovic	
4.Use of genetic algorithms for optimal design of electrical resistive furnaces insulation	615
Hristo Nenov and Borislav Dimitrov	
5.Communications in Realized Industrial Computer Networks	619
Viša Tasić, Dragan R.Milivojević, Vladimir Despotović, Darko Brodić, Marijana Pavlov, Ivana Stojković	
6.Information technology to calculate energy savings using solar panels and home appliances	623
Nanko Bozukov, Tanya Titova and Veselin Nachev	
7.Using Dashboards as tools to improve the process of decision making in healthcare	625
Jasmina Nedelkoska, Snezana Savoska and Emilija Taleska	
8.Preparation of data for visualization using SQL Server 2008	629
Emilija Taleska, Snezana Savoska, Jasmina Nedelkoska	
9.Statistical parameters of the first order for Rayleigh Fading with EGC Diversity combiner using MATLAB	633
Borivoje Milosevic, Mihajlo Stefanovic, Slobodan Obradovic and Srdjan Jovković	
10.Expert systems for managing asbestos in premises	637
Igor Nedelkovski, Boban Mircheski and Aleksandra Lozanovska	
11. Ontology-based Personalization and Recommender System in Digital Libraries	641
Daniela Kjurchievska	

Poster 7 - Electronics

1.Autonomous Inverters With Energy Dosing For Ultrasonic Applications	647
Nikolay Dimitrov Madzharov	
2.Virtual System for Magnetic Field Measurement	651
Nikola Draganov, Totka Draganova, Anatolii Aleksandrov	

3. Based on AMR Sensor Device for Contactless Measurement of AC Current	655
Nikola Draganov	
4. Design and Signal Processing Techniques on 0.18µm CMOS Hall Microsensors	659
Tihomir Takov, Ivelina Cholakova and Yavor Georgiev	
5. Investigation of the Defects Formation in Flexible Organic Light Emitting Devices by Thermal Activated Currents	663
Mariya Aleksandrova	
6. Incremental Encoder Macromodel for Educational Purpose	667
Marieta Kovacheva and Peter Yakimov	
7. Electrical Properties of Poly(Vinylidene Fluoride-COHexafluoropropylene) Nanocomposites with Nanoclays	671
Pavlik Rahnev, Dimitrina Kiryakova, Lyudmila Borisova and Atanas Atanassov	
8. Metal – Polymer Based Power Bulk Resistors	675
Pavlik Rahnev and Silviya Letskovska	
9. Modeling of high voltage periodically attenuating discharge in liquid with controllable high voltage switch thyatron	677
Milena Ivanova and Stefan Barudov	
10. Analysis and Design of Instrumentation Amplifiers	681
Ivailo Pandiev	
11. Pspice Simulation of Optoelectronic Circuits of Detectors	685
Hristo Sabev and Tsanko Karadzhov	
12. Subtraction Procedure for Removing the Baseline Drift from ECG Signals: Adaptation For Real Time Operation With Programmable Devices	687
Tsvetan Shoshkov and Georgy Mihov	
13. Investigation of Thin PZT and ZnO Piezoelectric Layers in Dynamic Mode for Application in MEMS	691
Georgi Kolev, Krassimir Denishev, Mariya Aleksandrova and Yordanka Dutsolova	
14. Sputtering of Thin Films on Flexible Substrates	695
Pavlik Rahnev, Silviya Letskovska, Dimitar Parachkevov and Kamen Seymenliyski	
15. Design and Realization of a small 10 Watt Forward Converter	699
Zoran Zivanovic and Vladimir Smiljakovic	

Poster 8 - Measurement Science and Technology

1. Vibration Measurement with Piezoelectric Transducer	705
Bozhidar Dzhudzhev, Veselka Ivancheva, Silviya Kachulkova and Ekaterina Gospodinova	
2. Examination of capacitive transducers and their use for measurement of small linear displacements	709
Veselka Ivancheva, Silvia Kachulkova, Bozhidar Dzhudzhev and Vladislav Slavov	
3. RADFET as a sensor and dosimeter of gamma-ray irradiation	713
Milić Pejović, Momčilo Pejović and Nikola Nešić	
4. New approach for designing high-performance controllers in electrical drives systems using Programmable Logic Devices	717
Vladimir Karailiev and Valentina Rankovska	

Poster 9 - Energy Systems and Efficiency

1. Analysis of the Mesh Voltage Calculation Method in the Presence of a Two-Layer Soil	723
Marinela Yordanova, Margreta Vasileva and Rositsa Dimitrova	

2.Design, Construction, Calibration and Use of A New Type of Electromagnetic Brake.....	727
Miroslav Bjekic, Milos Bozic, Marko Rosic, Marko Popovic, Dragisa Petkovic	
3.Energy Capability of Metal-Oxide Surge Arresters in Electric Power Lines 20 kV	731
Margreta Vasileva and Marinela Yordanova	
4.Model-experiment comparative analysis of roof type photovoltaic generator	735
Bohos Aprahamian and Milena Goranova	
5.Renewable Energy Sources and Tariffing of Electrical Power	739
Silvija Letskovska and Kamen Seymenliyski	
6.Experimental Verification of Algorithm for Indirect Domestic Load Recognition	743
Konstantin Gerasimov, Yulian Rangelov and Nikolay Nikolaev	
7.Functionalities Extension of the NASAVR Software For Small-Signal Stability of Electric Power Systems.....	747
Yulian Rangelov, Konstantin Gerasimov, Yoncho Kamenov and Krum Gerasimov	
8.Mechanical Design of High Voltage Overhead Transmission Lines With Thermal-Resistant Aluminum Alloy Conductors Considering the Heating From The Electrical Current	751
Yoncho Kamenov, Yulian Rangelov and Angel Varangov	
9.Optimization of Electric Resistance Furnace Using Backtracking Algorithm	755
Borislav Dimitrov, Marinela Yordanova and Hristo Nenov	
10.Heat-accumulation system powered by photovoltaic modules.....	759
Milena Goranova and Bohos Aprahamian	
11.Model Study of the Processes In Current Instrument Transformers For The Purposes of Relay Protection.....	763
Krum Gerasimov, Mediha Hamza, Margreta Vasileva and Anton Filipov	
12.Design of Photovoltaic plant for research purposes in University of Transport – Sofia	767
Ivan Milenov and Vasil Dimitrov	
13.Vector Analysis and Comparative Valuation of Precise and Approximate Non-Linear Models of Discrete Regulator with Reducing Input AC Voltage.....	771
Emil Panov, Emil Barudov and Stefan Barudov	
14.LED Technology in public lighting installations – facts or fiction.....	775
Andrej Djuretic, Nebojsa Arsic and Mile Petrovic	
15.Daily Load Curves for Different Months of Commercial Load Excluding Craft Stores and Shops	779
Lidija Korunovic and Marko Vuckovic	
16.Electromagnetic field analysis on salient poles synchronous motor in 3D.....	783
Blagoja Arapinoski, Mirka Popnikolova Radevska, Milan Cundev, Vesna Ceselkoska	
17.Numerical analysis and calculation of parameters of Three-Phase Induction Motor with Double Squirrel Cage	787
Blagoja Arapinoski, Milan Cundev and Mirka Popnikolova Radevska	

Poster 10 - Control Systems

1.Development of a system for power supply monitoring and autonomous ignition of gasoline generator.....	793
Goran Goranov and Iskren Kandov	
2.Bondsim Modeling and Simulation of Chaos in Cascade Connected Nonlinear Electrical Systems	797
Bojana M. Zlatkovic and Biljana Samardzic	
3.Further results on integer and non-integer order PID control of robotic system	801
Mihailo Lazarević, Srećko Batalov, Milan Cajić and Petar Mandić	

4. Investigating the behaviour of the welding manipulator tip	805
Svetlana Gerganova-Savova	
5. Neuro-Genetic Algorithm for Non-Destructive Food Quality Determination.....	809
Tanya Titova, Veselin Nachev, Chavdar Damyanov and Nanko Bozukov	
6.11 DoF inertial system for dynamics analysis of moving objects.....	813
Rosen Miletiev, Emil Iontchev, Ivaylo Simeonov, Rumen Yordanov	
7. Principles and Methods of Data Models Creation Within Automated Control Systems.....	817
Zoya Hubenova, Antonio Andonov, Vladimir Gergov	
8. Bond Graph Modelling and Simulation of the 3D Crane System Using Dymola.....	821
Dragan Antić, Dragana Trajković, Saša Nikolić, Staniša Perić and Marko Milojković	
9. Identification of Dynamic Processes with Artificial Neural Networks.....	825
Jordan Badev and Ivan Maslinkov	
10. AGV Guidance System Simulation with Lego Mindstorm NXT and RobotC	829
Violeta Kostova, Ramona Markoska and Mitko Kostov	

Poster 11 - Engineering Education

1. Teaching FPGA-Based CPU Cores and Microcontrollers	835
Valentina Rankovska	
2. Interactive Learning Module Implementing "Divide and Search" Procedure in Convolutional Encoders Analysis	839
Adriana Borodzhieva, Galia Marinova and Tzvetomir Vassilev	
3. GUI for Properties Measurement of Medical Images.....	843
Veska Georgieva and Olga Valchkova	
4. Realization of flying shear for laboratory experiments	847
Božić Miloš, Nebojša Mitrović and Marko Rosić	
5. Curricula Innovation of the Study Program in Environmental Protection Engineering	851
Tale Geramitcioski, Vangelce Mitrevski, Ilios Vilos and Pece Mitrevski	
6. English for specific purposes on Cloud Platform.....	855
Danica Milosevic and Borivoje Milosevic	
7. Online simulation of nonlinearity limitations in a single mode optical fiber	859
Kalin Dimitrov and Lidia Jordanova	
AUTHOR INDEX.....	861

Incremental Encoder Macromodel for Educational Purpose

Marieta Kovacheva¹ and Peter Yakimov²

Abstract – In this paper a macromodel of incremental rotary encoder is described. The basic characteristics and the behaviour of the encoders are studied. A digital electronic circuit generating the output signals of the incremental encoder and simulating its operation is designed. Results of the macromodel simulations are given.

Keywords – Rotary incremental encoder, Macromodelling, Digital circuits design, Simulation.

I. INTRODUCTION

In the past decade, the simulation has taken on an increasingly important role within electronic circuit design. The most popular simulation tool for this is PSpice A/D, which is available in multiple forms for various computer platforms. However, to achieve meaningful simulation results, designers need accurate models of many system components.

Simulation and modelling are investigations approaches which take place in different fields of scientific and applications developments [4, 5]. First the behaviour of a circuit or module is simulated and the parameters are adjusted and then they are verified by investigation over real object.

Harsh industrial applications often expose plant equipment to caustic chemical materials, resulting in premature deterioration and failure. Complex systems control requires complete cover of the behaviour of every industrial object and process in all working regimes. This could be achieved using the possibilities of the simulation and modelling. These methods have wide application in industry areas like electric power production, chemical manufacture, machine building and etc. where interruption and accident regimes creation in order to adjust the control equipment are impermissible.

The use of motion transducers has become commonplace and increasingly important to motion control systems designers in all sectors of manufacturing industries. As rapid advances in size, accuracy, resolution, and application sensitive mechanical packaging develops, close loop systems become more attractive to design engineers. The broad range of devices that are currently available can offer design engineers multiple solutions to their motion control needs.

Encoders enable design engineers to control motion by providing reliable feedback within the process loop. Optical rotary encoders are the most widely used method of

transforming mechanical rotary motion into electrical output.

Compact macromodels of different devices and in particular, incremental encoders are desired to speedup the simulation without sacrificing any of the required accuracy. One method to decrease simulation time and improve the convergence, without a significant loss of information, is by using behavioural macromodelling technique. Macromodelling is a way of providing macroscopic models of the corresponding devices.

Simulation investigations are of great importance for systems intended for applications in gas and oil production, chemical processing, grain and coal dust, and other hazardous environments.

Macromodels are very useful in education, where the students can investigate and study the basic characteristics of the encoders and the circuits processing their output signals.

Without any doubt macromodels of incremental encoders are necessary for simulating controllable motion systems. However, powerful simulation macromodels have not been available yet.

II. ENCODERS PRINCIPLES

A. Classification

Encoders are mechanical to electrical transducers whose output is derived by “reading” a coded pattern on a rotating disk or a moving scale. Encoders are classified by the:

- method used to read the coded element - contact or non-contact;
- type of output - absolute digital word or series of incremental pulses;
- physical phenomenon employed to produce the output - electrical conduction, magnetic, optical, capacitive.

In comparison to the absolute encoders the incremental ones have some advantages. Generally, incremental encoders provide more resolution at a lower cost than their absolute encoder analogs. They also have a simpler interface because they have fewer output lines. In a simple form, an incremental encoder would have 4 lines: 2 quadrature (A & B) signals, and power and ground lines. A 12 bit absolute encoder, by contrast, would use 12 output wires plus a power and ground line.

B. Theory of operation

The principle of incremental encoder operation is generation of a symmetric, repeating waveform that can be used to monitor the input motion. The basic components of all optical incremental encoders are the light source, light shutter system, light sensor, and signal conditioning electronics.

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These components will be housed and assembled to various mechanical assemblies, either rotary or linear in design depending on how motion will be monitored. The encoder mechanical input operates the light shutter which modulates the intensity of the light at the sensor. The sensors electrical output is a function of the incident light. The encoders electrical output is produced from the sensor output by the signal conditioning electronics and can be either:

- a sine-wave;
- a shaped, square-wave;
- a series of equally spaced pulses produced at regular points on the waveform.

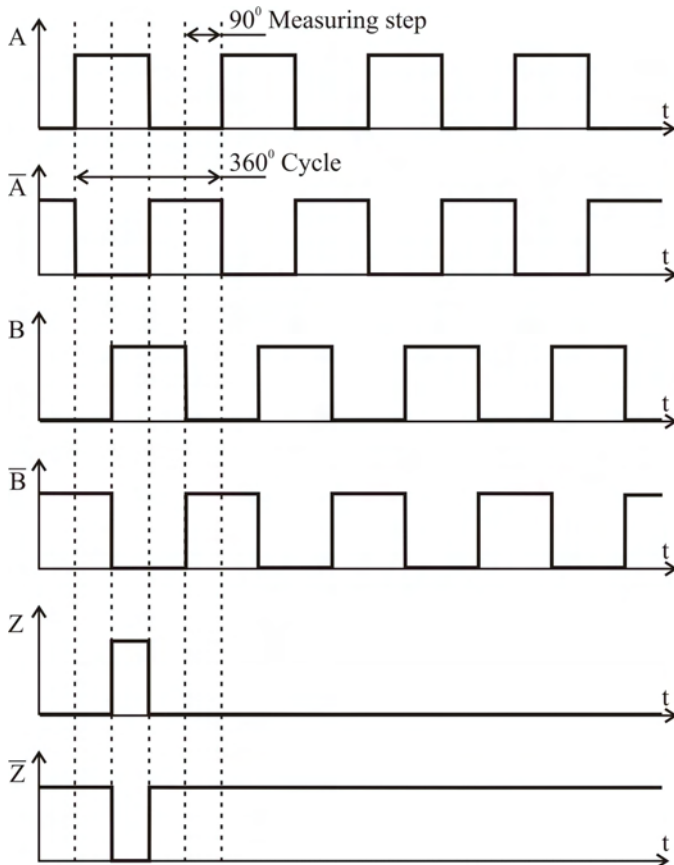


Fig. 1. Output signals in CCW rotation

An incremental encoder produces a series of square waves as it rotates. The number of square wave cycles produced per one turn of the shaft is called the encoder resolution. Incremental encoders work by rotating a code disc in the path of a light source. The code disc acts like a shutter to alternately shut off or transmit the light to a photodetector. The pulse range of an encoder is dictated by the number of tracks of clear and opaque lines located on the disc. Thus, the resolution of the encoder is the same as the number of lines on the code disc. A resolution of 360 means that the encoder code disc will have 360 lines on it and one turn of the encoder shaft will produce 360 complete square wave cycles, each cycle indicating one degree of shaft rotation. Since the resolution is "hard coded" on the code disc, optical encoders are inherently very repeatable and, when well constructed,

very accurate. The square wave output is inherently easy for digital signal processing techniques to handle.

Incremental encoders are usually supplied with two channels (A & B) that are offset from one another by 1/4 of a cycle (90 electrical degrees). This signal pattern is referred to as quadrature and allows the user to determine not only the speed of rotation but its direction as well. By examining the phase relationship between the A and B channels can be determined that A leads B for counterclockwise (CCW) rotation of the input shaft as it is shown on Fig. 1.

Generally in addition to the signals A and B, their inverse forms are also available. The complete signals set includes also a "zero" pulse – Z and its inverse form. This signal is generated using another track on the disc that has only one opaque line. Signal Z rises high once per one turn of the shaft.

The relationship of the encoder output signals for turning clockwise (B leads A) is shown on Fig. 2.

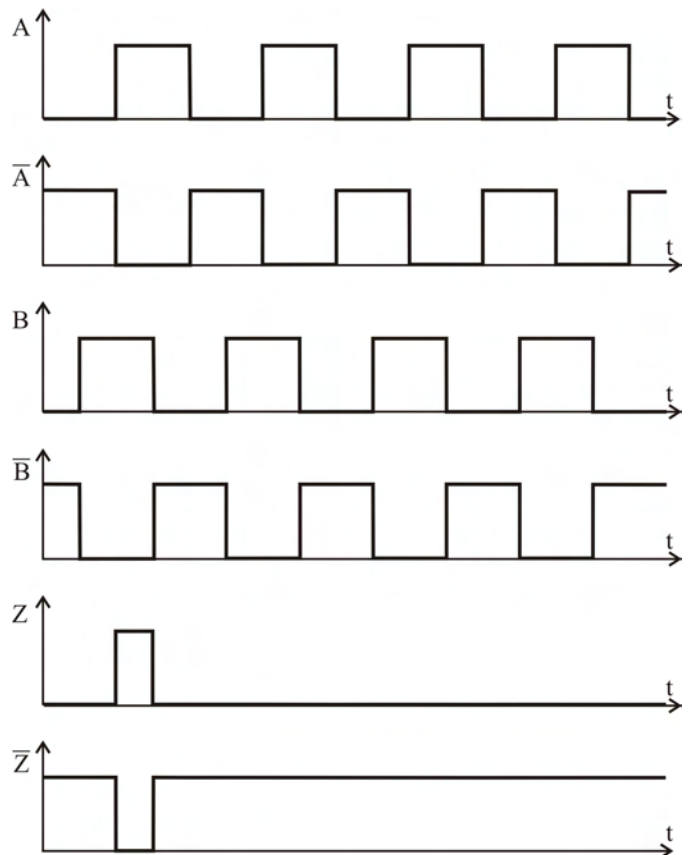


Fig. 2. Output signals in CW rotation

Except for discrimination between direction of movement (CW versus CCW), the quadrature relation allows for error detection in high vibration environments and higher resolution by using edge detection. With quadrature detection the controller can derive 1X, 2X or 4X the basic code disc resolution. 10,000 counts per turn can be generated from a 2500 cycle, two-channel encoder by detecting the Up and Down transitions on both the A and B channels as it is shown on Fig.1. In this case the measuring step is 90 electrical degrees. With a quality disc and properly phased encoder, this 4X signal will be accurate to better than 1/2 count.

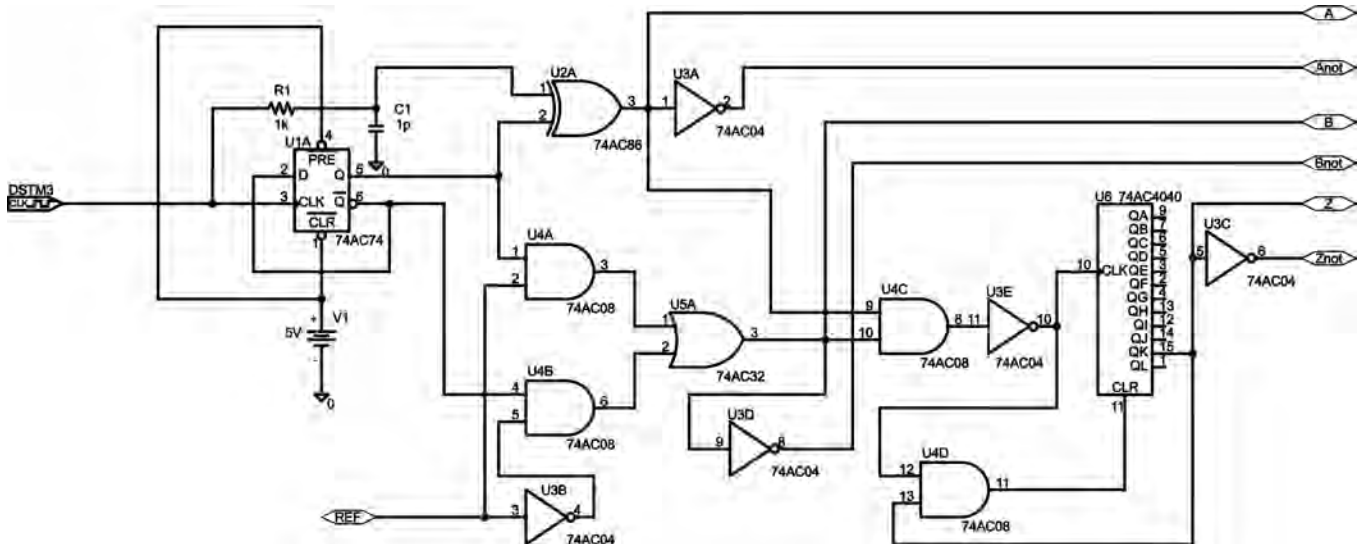


Fig. 3. Macromodel electric circuit

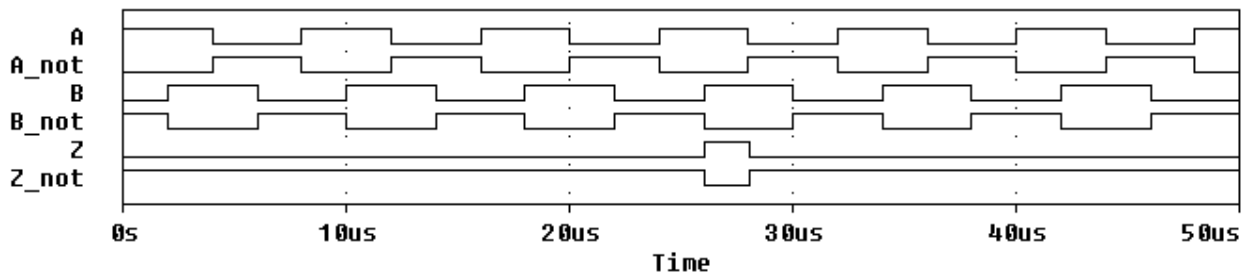


Fig. 4. Simulation results in CCW rotation

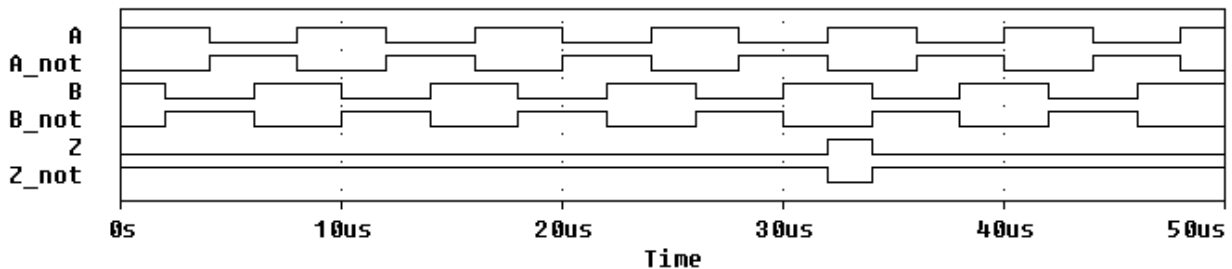


Fig. 5. Simulation results in CW rotation

III. MACROMODEL DEVELOPMENT

The macromodel electric circuit is designed using standard advanced CMOS combinational and sequential logic circuits and is shown on Fig. 3. It simulates the action of a rotary encoder. The frequency of the output signals A and B is set by the digital stimulus DSTM3 and can be adjusted editing its model parameters. This is important for the flexibility of the macromodel because this is the possibility to set the value of the speed of the movement. The main part of the circuit is the generation of the signals with quadrature relation. This is obtained using the D flip-flop (U1A) acting as a frequency

divider and the XOR logic gate (U2A). To the inputs of the same gate are applied the signal with the clock frequency and the one with its half value. In this way the signal produced by the XOR gate and the signal produced by the normal output of the flip-flop are with 90 electrical degrees phase shift. If the initial state of the flip-flop is high then the signal from its normal output will lead the signal produced by the XOR gate. In the same time the signal obtained from the inverted output of the flip-flop will lag the output signal of the XOR gate. If it is accepted that the output of the XOR gate produces the signal A, then there must be a part of the circuit that will simulate the signal B and the change of the direction of the rotation. Thus there is a two-input multiplexer realized with the AND gates U4A and U4B, the inverter U3B and the OR

gate U5A. The signal B is obtained from the output of the multiplexer. An external logic signal applied to the input REF determines the relation between the signals A and B. When this input is tied low signal B will be produced by the inverted output of the flip-flop and then A will lead B as it is shown on Fig. 4. In contrary when a high level is applied to the REF input the signal B will lead the signal A as it is depicted in Fig. 5. Thus the change of the logic level applied to the REF input simulates the change of the direction of the rotation. In order to have the inverted forms of the signals A and B, two inverters are added. The logic gate U3A produces the inverted form A_not and the logic gate U3D – the inverted form B_not. The complete set of signals of the rotary encoder includes the “index” signal Z. There is another part of the circuit that generates it. The circuit consists of the 12-stage binary ripple counter 74AC4040 (U6) and the logic gates U4C, U4D, U3E and U3C. This circuit must produce a pulse on every previously defined number of pulses on outputs A and B. So this pulse is generated once per one turn of the shaft of the encoder. Because the counter advances on the high-to-low transition of CP input and to simulate correctly the operation of the rotary encoder the clock pulses for the counter are produced by AND function of the signals A and B followed by inversion. The number of cycles of A and B signals per one turn of the encoder shaft is simulated by choosing the output of the counter from which the Z signal will be derived. In the shown example the Z signal is derived from the output QK which means that there will be 1024 cycles per one turn. The biggest number of cycles that can be simulated using this macromodel is 2048 if the signal Z is derived from the output QL. Any less number equal to a power of two can be obtained. In this way the resolution of the encoder is simulated. To shape the Z pulse an additional logic for resetting the counter is used. The width of this pulse must be equal to one quarter of the cycle. To achieve it the AND gate U4D produces the active high level for the master reset input of the counter. Since the chosen output has risen after the falling edge of the clock signal one of the inputs of the AND gate receives logic one. The output of the counter will hold its high level till the clock pulse raises its state. This time interval will continue one quarter of the cycle and after low-to-high transition of the clock pulse logic “1” will be applied to the other input of U4D. At that moment MR input of the counter will receive its active level and all outputs will be cleared. The inverter U3C produces the inverted form of the signal Z_not. So all output signals of the incremental encoder are generated and the simulated timing diagrams are shown on Figs. 4 and 5.

IV. CONCLUSION

In this paper an incremental rotary encoder macromodel is presented. It simulates the operation of the encoder in both directions of rotation. The full set of output signals is generated. The simulated timing diagrams of the signals correspond to the real ones. This macromodel has been used in the laboratory work of the students at the Technical university of Sofia. The macromodel can be used in design

and studying electronic circuits for processing incremental rotary encoder signals.

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