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Since 2005, he has been working in the corporate research center of Philips in Eindhoven. He has been involved in strategic innovation in application specific integrated circuits (ASICs) for Philips Healthcare businesses, with broad experience in the definition, prototyping and start of mass production of complex systems on a chip. He has been working as system architect and project lead for several multi-disciplinary research projects related to medical imaging, patient monitoring and emerging medical technologies. Since 2012, he is the cluster lead of ASIC research for medical and bio applications within Philips Research. In this function, he has been responsible for the short and long term strategy definition of the ASIC related research and for the operational management of the cluster. Sotir has about 20 peer reviewed IEEE and Elsevier papers and is author of more than 20 patent applications.